

18/06/2013

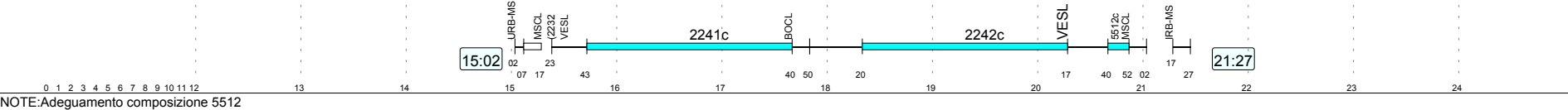
Ma
10

Riposo

	Rip.
	53:06

19/06/2013

Me
223216
11

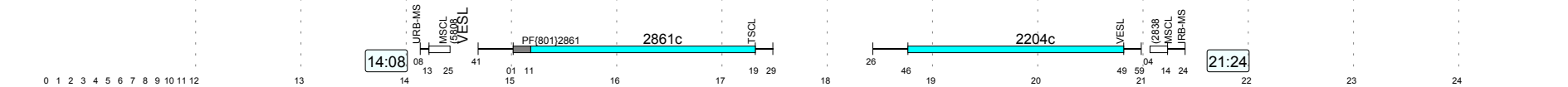


NOTE:Adeguamento composizione 5512

Lav	
06:25	Not
	No
Rip.G	
16:41	

20/06/2013

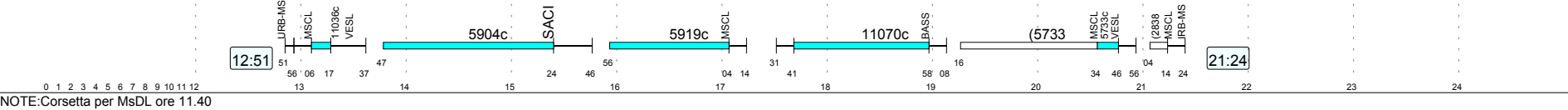
Gi
223224
12



Lav	
07:16	Not
	No
Rip.G	
15:27	

21/06/2013

Ve
223215
13

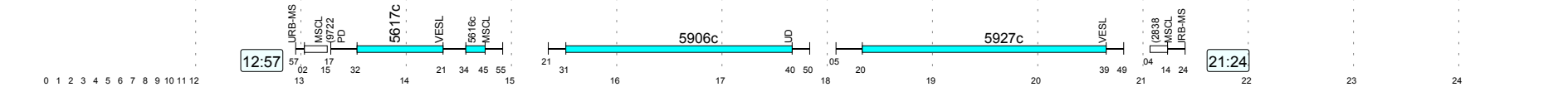


NOTE:Corsetta per MsDL ore 11.40

Lav	
08:33	Not
	No
Rip.G	
15:33	

22/06/2013

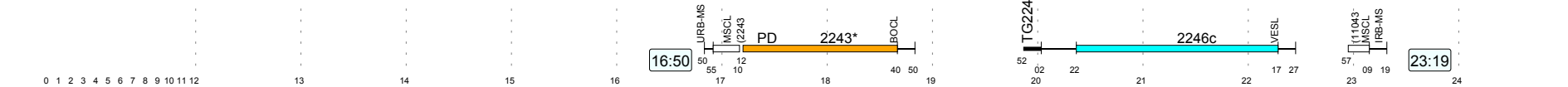
Sa
223202
14



Lav	
08:27	Not
	No
Rip.G	
19:26	

23/06/2013

Do
223339
15



Lav	
06:29	Not
	No
Rip.G	
00:00	

24/06/2013

Lu
16

Riposo

	Rip.
	65:49

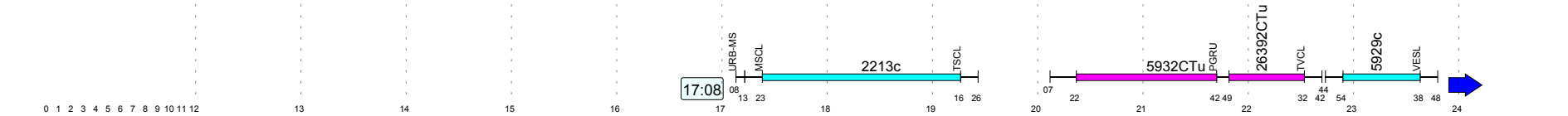
25/06/2013

Ma
17

INTERVALLO

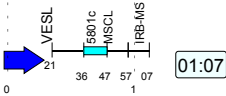
26/06/2013

Me
223231
18



27/06/2013

Gi
223231
19



Lav	
07:59	
	Not
	Si
Rip.G	
35:59	

28/06/2013

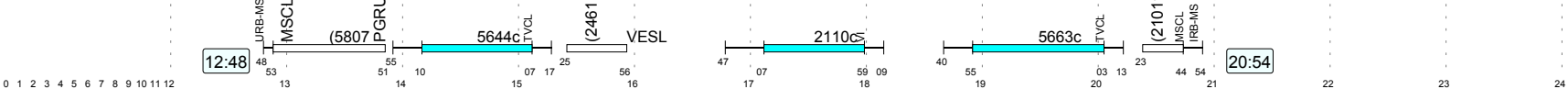
Ve
223227
20



Lav	
07:56	
	Not
	No
Rip.G	
15:46	

29/06/2013

Sa
223440
21



Lav	
08:06	
	Not
	No
Rip.G	
00:00	

30/06/2013

Do
22

Riposo Quantitativo

	Rip.
	66:14

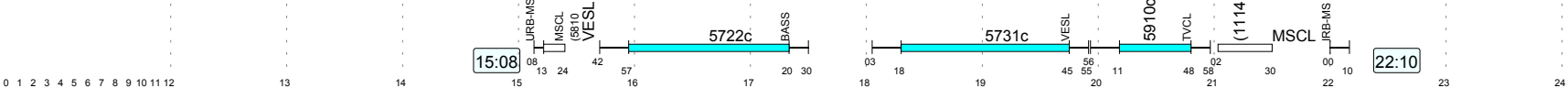
01/07/2013

Lu
23

INTERVALLO

02/07/2013

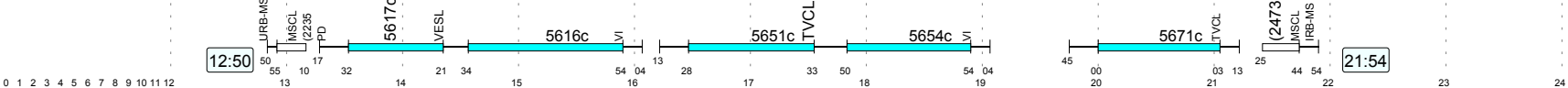
Ma
223229
24



Lav	
07:02	
	Not
	No
Rip.G	
14:40	

03/07/2013

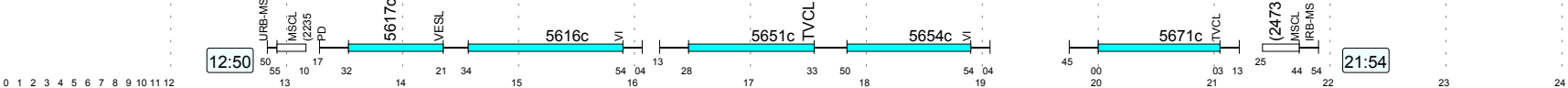
Me
223221
25



Lav	
09:04	
	Not
	No
Rip.G	
14:56	

04/07/2013

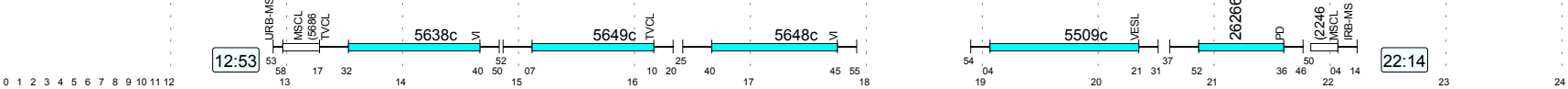
Gi
223221
26



Lav	
09:04	
	Not
	No
Rip.G	
14:59	

05/07/2013

Ve
223220
27



Lav	
09:21	
	Not
	No
Rip.G	
00:00	

06/07/2013

Sa
28

Riposo Weekend

	Rip.
	62:37

07/07/2013

Do

29

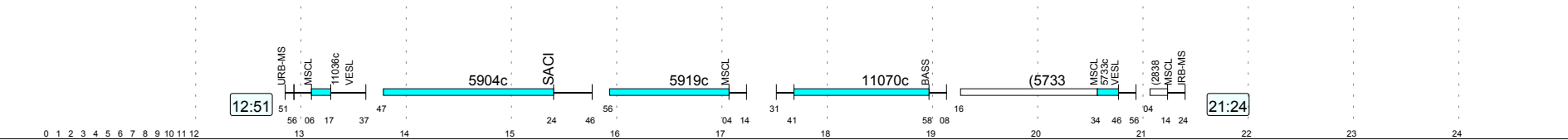
INTERVALLO

08/07/2013

Lu

223215

30



NOTE:Corsetta per MsDL ore 11.40

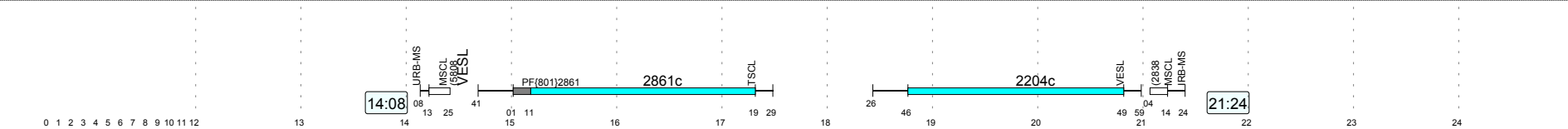
Lav	
08:33	
	Not
	No
Rip.G	
16:44	

09/07/2013

Ma

223224

31



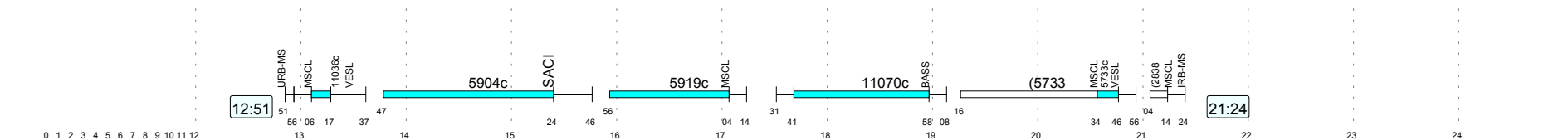
Lav	
07:16	
	Not
	No
Rip.G	
15:27	

10/07/2013

Me

223215

32



NOTE:Corsetta per MsDL ore 11.40

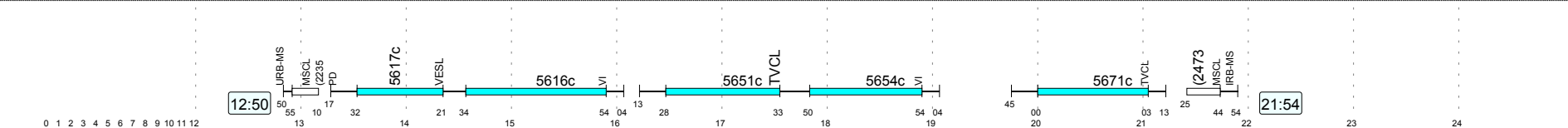
Lav	
08:33	
	Not
	No
Rip.G	
15:26	

11/07/2013

Gi

223221

33



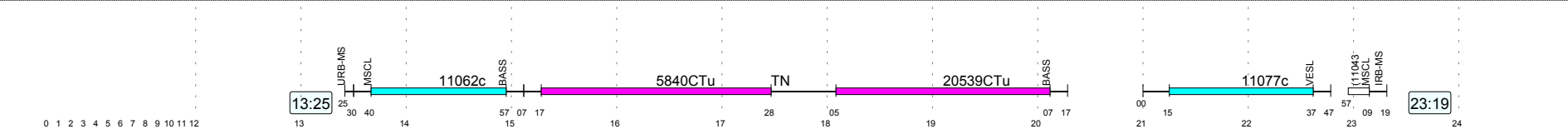
Lav	
09:04	
	Not
	No
Rip.G	
15:31	

12/07/2013

Ve

223225

34



Lav	
09:54	
	Not
	No
Rip.G	
00:00	

13/07/2013

Sa

35

Riposo Weekend

	Rip.
	61:31

14/07/2013

Do

36

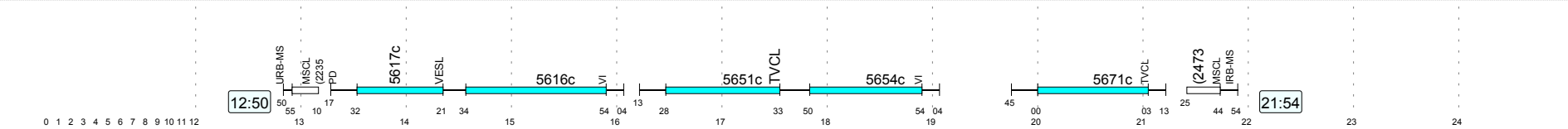
INTERVALLO

15/07/2013

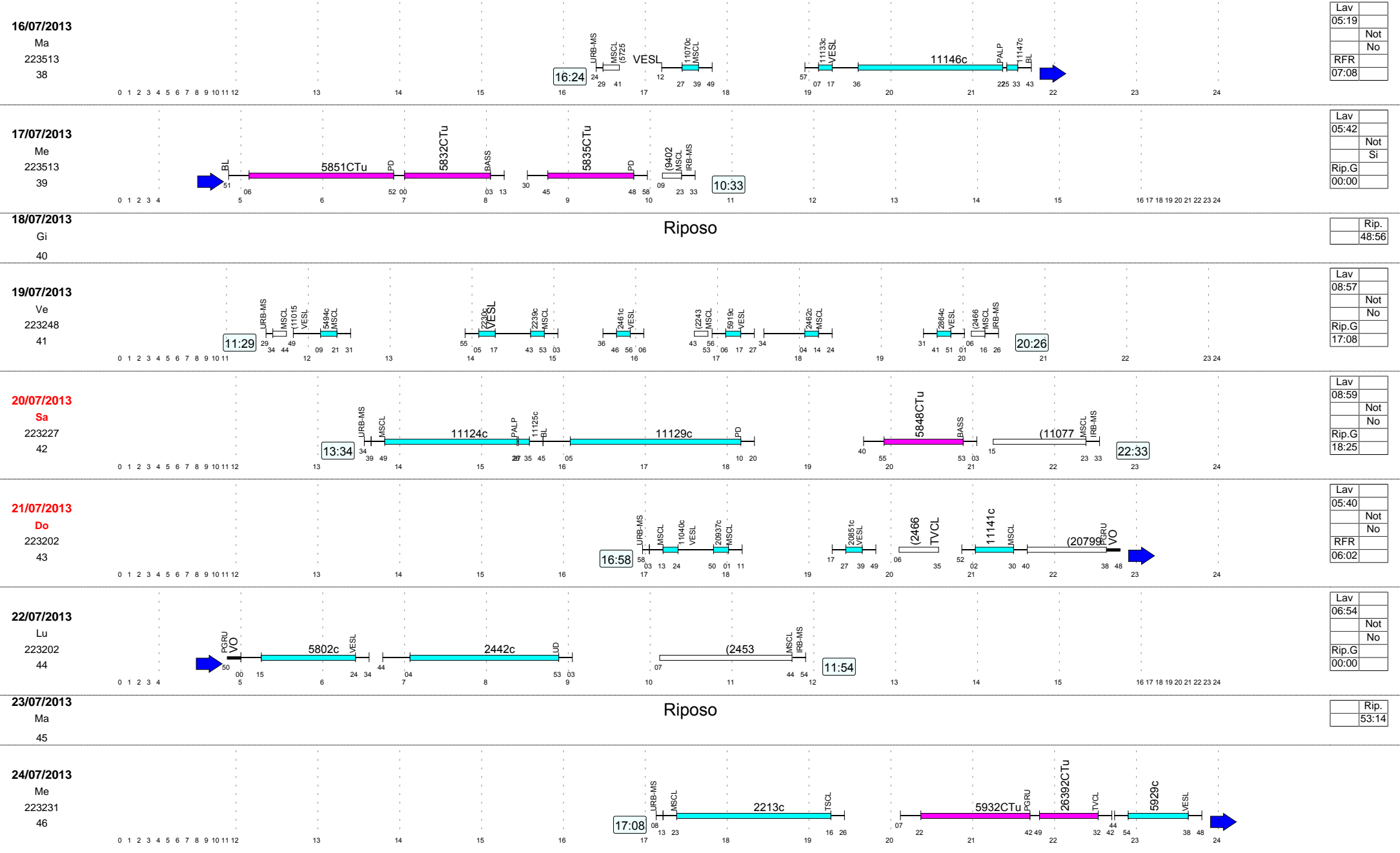
Lu

223221

37



Lav	
09:04	
	Not
	No
Rip.G	
18:30	



Timing diagram for a 100MHz system. The diagram shows a clock signal (blue arrow) and four data signals: VESL (green), SDO1c (red), MSCL (blue), and I2B-MS (purple). The signals are sampled at 10ns intervals. A callout box shows a value of 01:07.

INTERVALLO

Riposo Weekend

FERIE

FERIE

FERIE

FERIE

INTERVALLO

INTERVALLO

Riposo

FERIE

FERIE

FERIE

FERIE

FERIE

INTERVALLO

10/08/2013	Riposo Weekend		Rip.
Sa			60:00
63			
11/08/2013	FERIE		
Do			
64			
12/08/2013	NON ASSEGNATO		
Lu			
65			
13/08/2013	NON ASSEGNATO		
Ma			
66			
14/08/2013	NON ASSEGNATO		
Me			
67			
15/08/2013	NON ASSEGNATO		
Gi			
68			
16/08/2013	NON ASSEGNATO		
Ve			
69			
17/08/2013	Riposo Weekend		Rip.
Sa			00:00
70			
18/08/2013	NON ASSEGNATO		
Do			
71			
19/08/2013	NON ASSEGNATO		
Lu			
72			
20/08/2013	NON ASSEGNATO		
Ma			
73			
21/08/2013	NON ASSEGNATO		
Me			
74			
22/08/2013	NON ASSEGNATO		
Gi			
75			
23/08/2013	NON ASSEGNATO		
Ve			
76			
24/08/2013	Riposo Quantitativo		Rip.
Sa			00:00
77			
25/08/2013	NON ASSEGNATO		
Do			
78			
26/08/2013	NON ASSEGNATO		
Lu			
79			
27/08/2013	NON ASSEGNATO		
Ma			
80			

PERIODO: 09/06/2013 - 07/09/2013 IMPIANTO: MS-RB TURNO: DPR Mestre - Bordo					
28/08/2013	NON ASSEGNATO				
Me					
81					
29/08/2013	NON ASSEGNATO				
Gi					
82					
30/08/2013	NON ASSEGNATO				
Ve					
83					
31/08/2013	Riposo Weekend				
Sa		<table><tr><td></td><td>Rip.</td></tr><tr><td></td><td>00:00</td></tr></table>		Rip.	
	Rip.				
	00:00				
84					
01/09/2013	NON ASSEGNATO				
Do					
85					
02/09/2013	NON ASSEGNATO				
Lu					
86					
03/09/2013	NON ASSEGNATO				
Ma					
87					
04/09/2013	NON ASSEGNATO				
Me					
88					
05/09/2013	NON ASSEGNATO				
Gi					
89					
06/09/2013	Riposo				
Ve		<table><tr><td></td><td>Rip.</td></tr><tr><td></td><td>00:00</td></tr></table>		Rip.	
	Rip.				
	00:00				
90					
07/09/2013	NON ASSEGNATO				
Sa					
91					